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# ADC Histogram Test Using Triangular Stimulus Signals

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## Abstract

This paper focuses on tackling the uncertainty in ADC testing estimates using the Histogram Method with a triangular stimulus instead of a sinusoidal one as is standard practice. It introduces expressions to calculate the standard deviation of transition voltages and code bin widths.

Keywords: Analog-to-Digital Converter; Histogram Method; Testing; Uncertainty; Noise

## 1. Introduction

Characterization of analog to digital converters (ADCs) involves several test methods, among which is the Histogram Test Method stands as one of the most widely utilized approaches for estimating the ADC transfer function [1]-[7]. Traditionally, this method has been employed with sinusoidal stimulus signals [8]-[9], as many other methods do [10]-[12], providing dynamic characterization of the converter. However, recent advances have led to the use of triangular waves for static testing of ADCs through a method known as "Ramp Vernier" [13]-[16].

In any measurement process, it is crucial to estimate not only the transition voltages and code bin widths of an ADC but also their associated uncertainties. This paper contributes to addressing this issue, focusing specifically on the case of using triangular stimulus signals in conjunction with the Histogram procedure. Previous research has been carried out on the bias of this test method, exploring optimal overdrive levels to minimize the impact of additive noise on the error of transition voltages and code bin widths [17].

The paper's structure is as follows: In Section 2, the Histogram test method is described, and the variables employed to describe the ADC transfer function and stimulus signal are introduced. Moving to Section 3, the variance of the transition voltages is computed, followed by the computation of the variance of the code bin widths in Section 4. Finally, Section 5 presents the conclusions drawn from the research.

Overall, this paper significantly contributes to the understanding of uncertainty estimation in ADC testing, particularly when employing triangular stimulus signals and the Histogram Test Method. This research opens up new possibilities for more accurate and reliable ADC characterizations, shedding light on potential improvements and optimizations in the testing process.

## 2. Histogram Test Method

The Histogram procedure [18] is a fundamental approach used in the characterization of analog to digital converters (ADCs). This method involves subjecting the ADC under test to acquire an extensive set of samples from a known input signal, typically a sinusoid. Subsequently, the digital codes corresponding to each of these samples are utilized to construct a cumulative histogram (CH[k]). The array CH[k] represents the count of samples with digital codes equal to

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or lower than each of the possible ADC output codes *k*. Essentially, it provides a distribution of how frequently different output codes occur in the ADC's response to the given input signal.

The cumulative histogram, CH[k], is an array consisting of  $2^N$  values, where N represents the number of bits of the ADC. Each value in the CH[k] array corresponds to the number of samples whose digital code falls within the specific range defined by the ADC output codes. For instance, CH[0] would indicate the number of samples with the lowest digital code (all 0s), CH[1] would represent the number of samples with the next digital code (0001 in binary), and so on, up to  $CH[2^{N}-1]$ , which would represent the highest digital code (all 1s).

By analyzing the cumulative histogram, engineers and researchers can gain valuable insights into the behavior of the ADC, including its linearity, resolution, and potential non-idealities. The shape and characteristics of the histogram provide essential information about the ADC's transfer function, allowing for the estimation of critical parameters such as Differential Non-Linearity (DNL) and Integral Non-Linearity (INL).

Overall, the Histogram procedure serves as a crucial tool in ADC testing and characterization, enabling the evaluation of its performance and aiding in the identification of potential issues or optimizations in the conversion process. With this method, engineers can better understand the ADC's response to different input signals and ensure its accuracy and reliability in various applications.

From this array, the ADC transition voltages are estimated using

$$T[k] = 2A \frac{CH[k-1]}{M} - A$$
,  $k = 1...2^{N} - 1$ ,.....(1)

and the code bin widths using

$$W[k] = 2A \frac{H[k]}{M}$$
,  $k = 1...2^{N} - 2$ ,....(2)

where M is the number of samples and H[k] is the number counts of the histogram which is the number of samples with each of the possible ADC output codes k. The relation between the value of the histogram and the cumulative histogram is

$$H[k] = CH[k] - CH[k-1]$$
,  $k = 1...2^{N} - 1$ .....(3)

In the traditional histogram method for ADC characterization, a full-scale periodic stimulus signal is applied to the ADC under test. This stimulus signal spans the entire input voltage range of the ADC and is applied continuously. Simultaneously, a specific number of samples are acquired from the ADC's output at a constant rate, but asynchronously with respect to the stimulus signal.

The crucial step in this method involves determining the ADC's transition voltages, which are the boundaries that separate different output codes. These transition voltages are essential for understanding the ADC's behavior, linearity, and resolution.

To obtain the ADC transition voltages, a comparison is made between the number of samples acquired in each code bin (ranges of output codes) and the number that would be expected in an ideal ADC. In an ideal ADC, the distribution of samples in each code bin would be perfectly uniform due to the uniform distribution of input voltages across the ADC's range.

To compute the expected number of samples in each code bin, knowledge of the probability distribution of the sample voltages is required. In other words, the value of the input signal at the precise moment of sampling needs to be known. This information is crucial to accurately estimate the expected distribution of samples in an ideal scenario.

However, in practice, the exact input signal value at the sampling instant is not always accessible, and this introduces uncertainty in the estimation of the ADC transition voltages. Various factors, such as noise [19], jitter [20], and non-idealities in the sampling process [21]-[24], can further contribute to this uncertainty. As a result, researchers and engineers often resort to statistical methods and multiple samples to improve the accuracy of the transition voltage estimation.

Overall, the traditional histogram method plays a fundamental role in ADC testing, as it allows engineers to analyze the ADC's performance and understand its response to the input stimulus. By estimating the transition voltages and evaluating the distribution of samples, valuable insights into the ADC's linearity and other important characteristics can be obtained, facilitating the optimization and refinement of ADC designs for various applications.

Consider each sample j (j = 0, 1, ..., M - 1) ideally acquired at instant  $t_j$ . Without loss of generality the time origin can be set to the ideal sampling instant of the first sample ( $t_0 = 0$ ). The phase of the samples ( $\gamma_j$ ), relative to the stimulus signal of frequency  $f_i$  is thus

$$\gamma_j = 2\pi f \cdot t_j + \varphi \dots (4)$$

where  $\varphi$  represents the phase of the stimulus signal at the ideal instant of acquisition of the first sample. The value of the sinusoidal stimulus signal in the sampling instant of sample *j* can be written as

where *d* and *A* are the stimulus signal offset and amplitude. The "tri(*x*)" function represents a general triangular wave function spanning from  $-\pi$  to  $\pi$  in *x* and from -1 to 1 in amplitude:

$$\operatorname{tri}(x) = \begin{cases} 1 - 4\left\langle \frac{x}{2\pi} \right\rangle &, \ 0 < \left\langle \frac{x}{2\pi} \right\rangle \le \frac{1}{2} \\ 4\left\langle \frac{x}{2\pi} \right\rangle - 3 &, \ \frac{1}{2} < \left\langle \frac{x}{2\pi} \right\rangle < 1. \end{cases}$$
(6)

The values of the sampled voltages  $(v_j)$  are equal to the value of the stimulus signal in the sampling instant  $(x_j)$  plus the input-equivalent wideband noise  $(n_v)$ .

$$v_j = n_v + d - A \cdot \operatorname{tri} \left( 2\pi f \cdot t_j + \varphi \right) \dots (7)$$

To simplify the computations, some normalizations are made. Let  $u_i$  be the normalized sample voltage.

where *n* is the normalized input-equivalent wideband noise and  $\gamma_j$  is the sample phase.

Next, we will focus on the estimation of the transition voltages made from the cumulative histogram array.

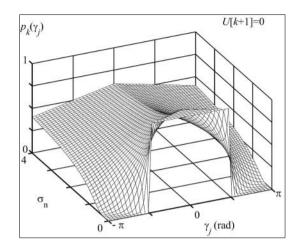
### 3. Variance of the Transition Voltages

#### 3.1. General sampling

This section introduces the calculation of the probability distribution for the number of counts in the histogram, considering a general type of sampling, whether it is random, synchronous, or asynchronous. The subsequent section will focus on the specific scenario of asynchronous sampling.

Moreover, taking into account that the input-equivalent noise follows a normal distribution with a mean of zero and a standard deviation denoted by  $\sigma_n$ , we can derive the probability density function (p.d.f.) of the sample voltages ( $u_i$ ).

Figure 1 represents the probability that a sample belongs to a class of the cumulative histogram as a function of the sample phase ( $\gamma$ ) and of the input-equivalent noise standard deviation ( $\sigma_n$ ) for the case of U[k+1] = 0 (transition voltage equal to the DC value of the stimulus signal).



**Figure 1** Representation of the probability that a sample belongs to a class of the cumulative histogram as a function of the sample phase ( $\gamma_1$ ) and of the input-equivalent noise standard deviation ( $\sigma_n$ ). Example with U[k+1]=0 (transition voltage equal to the DC value of the stimulus signal)

The probability that a sample j belongs to a class k of the cumulative histogram ( $p_k$ ) is equal to the probability that the normalized sample voltage is between normalized transition voltage U[k] and U[k+1]:

Consider now a variable *w<sub>k</sub>* that takes the value 1 if a sample belongs to class *k* of the cumulative histogram and 0 if not.

This variable has a binomial distribution with mean pk and variance  $p_k \cdot (1 - p_k)$ . The number of counts in class k of the histogram ( $c_k$ ) is the sum of variable  $w_k$  for all the samples. The p.d.f. of  $c_k$  is the convolution of the p.d.f. of the M variables  $w_k$ , because they are independent.

$$f_{c_{k}}(c \mid \varphi) = f_{w_{k}}(w \mid \gamma_{0}) * \dots * f_{w_{k}}(w \mid \gamma_{M-1})(c)$$
.....(12)

Note that now, the p.d.f. of the number of counts is conditional to the initial phase  $\varphi$  and not to the sampling instant  $\gamma_i$ . The conditional mean of  $c_k$  is the sum of the conditional means of the variables  $w_k$  for each sample. The same is true for the conditional variance.

The total p.d.f. of the number of counts can be obtained by integrating the product of the conditional p.d.f. with the p.d.f. of the initial stimulus signal phase ( $f_{\varphi}$ ) [25].

$$f_{c_{k}}(c) = \int_{-\infty}^{\infty} f_{c_{k}}(c \mid \varphi) \cdot f_{\varphi}(\varphi) \cdot d\varphi$$
.....(13)

From the total p.d.f.  $f_{c_k}(c)$ , it is possible to determine the total mean,

and variance of the number of counts:

#### 3.2. Asynchronous sampling

Now, let's take into account that the sampling process is carried out asynchronously with respect to the stimulus signal, meaning that the initial phase of the stimulus signal is not under control. In this context, we consider that this initial phase follows a uniform distribution, spanning from 0 to  $2\pi$ .

Additionally, if the frequencies of both the sampling clock and the stimulus signal are chosen deliberately and are not harmonically related to each other, a desirable outcome is achieved. Specifically, the ideal sample phases become uniformly spaced. This alignment of ideal sample phases provides a favorable condition for accurate and reliable measurements during the asynchronous sampling procedure.

The ideal sample phases are uniformly spaced:

With some manipulation an expression for the mean

and for the variance

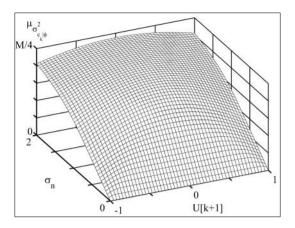
can be easily derived from (15), (16) and (17). These expressions are written in terms of the number of samples acquired (*M*) and the probability that a sample belongs to a class of the cumulative histogram ( $p_k$ ). The value of this probability depends, in turn, on the normalized input-equivalent noise standard deviation ( $\sigma_n$ ), on the normalized transition voltage (U[k]) and sample phase ( $\gamma$ ).

The expression for the variance was divided into two terms: the mean of the conditional variance ( ${}^{\mu_{\sigma_{x}}}_{\sigma_{x}}$ ) and the variance of the conditional mean ( ${}^{\sigma_{\mu_{x}}}$ ).

### 3.3. Mean of the conditional variance

In this paragraph we analyse the mean of the conditional variance. Inserting (10) into (18) leads to

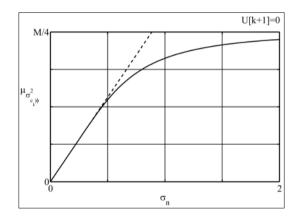
which is depicted in Figure 2.



**Figure 2** Representation of the term  $\mu_{\sigma_{q,w}^2}$  as a function of the standard deviation of the input-equivalent noise ( $\sigma_n$ ) and of the normalized transition voltage

The term  $\mu_{\sigma_{q_{i}e}}$  of the variance of the number of counts of the histogram is maximum for a transition voltage equal to the stimulus signal offset (U[k]=0). Ideally this would be the middle of the ADC input range. In this case the dependence

of  $\mu_{\sigma_{c_k \varphi}}$  on the standard deviations of input-equivalent noise is represented in Figure 3.



**Figure 3** Representation of the term  $\mu_{\sigma_{a,w}^2}$  as a function of the standard deviation of the input-equivalent noise ( $\sigma_n$ ) for a null value of normalized transition voltage. The dotted line represents the approximation for small values of input-equivalent noise standard deviation given by (27)

As the input-equivalent noise standard deviation goes to 0, the mean of the conditional variance is directly proportional to that same standard deviation. To prove that we will compute the following limit:

$$\lim_{\sigma_n \to 0} \frac{\mu_{\sigma_{ck}^2 \varphi}}{\sigma_n} = \frac{M}{2\pi \sqrt{\pi}} \lim_{\sigma_n \to 0} \int_{-\pi}^{\pi} \frac{\sqrt{\pi}}{4 \cdot \sigma_n} \cdot \left[ 1 - erf^2 \left( \frac{U[k+1] + tri(\varphi)}{\sqrt{2} \cdot \sigma_n} \right) \right] d\varphi \qquad (20)$$

Introducing now variable *x*:

and inserting it into (20), leads to

$$\lim_{\sigma_n \to 0} \frac{\mu_{\sigma_{c_k \varphi}^2}}{\sigma_n} = \frac{M}{\pi \sqrt{\pi}} \lim_{\sigma_n \to 0} \int_{U[k+1]-1}^{U[k+1]+1} \frac{\sqrt{\pi}}{4 \cdot \sigma_n} \frac{\pi}{2} \left[ 1 - erf^2 \left( \frac{x}{\sqrt{2} \cdot \sigma_n} \right) \right] \cdot dx \qquad (22)$$

## To obtain (22) we rewrote (21) as

$$\varphi = atri(x - U[k+1]) \dots (23)$$

The derivative is  $\partial \varphi / \partial x = \pi/2$ .

The limits of the integral in (22) can be extended to infinite for small values of  $\sigma_n$ :

$$\lim_{\sigma_n \to 0} \frac{\mu_{\sigma_{x, \forall \varphi}^2}}{\sigma_n} = \frac{M}{\pi \sqrt{\pi}} \lim_{\sigma_n \to 0} \int_{-\infty}^{\infty} \frac{\sqrt{\pi}}{4 \cdot \sigma_n} \frac{\pi}{2} \left[ 1 - erf^2 \left( \frac{x}{\sqrt{2} \cdot \sigma_n} \right) \right] \cdot dx$$
(24)

Considering that

$$\lim_{\sigma_n \to 0} \int_{-\infty}^{\infty} \frac{\sqrt{\pi}}{4 \cdot \sigma_n} \left[ 1 - erf^2 \left( \frac{x}{\sqrt{2} \cdot \sigma_n} \right) \right] \cdot dx = 1$$
.....(25)

we have

which demonstrates that we can approximate the mean of the conditional variance by

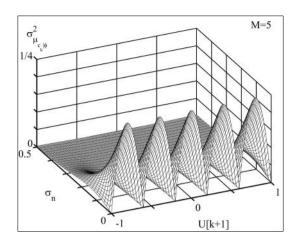
$$\mu_{\sigma_{c_k|\varphi}^2} = \frac{M}{2\sqrt{\pi}}\sigma_n \qquad (27)$$

For large values of standard deviation, the term  $\mu_{\sigma_{q,\psi}^2}$  approaches *M*/4. We can thus write an expression that gives us the upper limit of the term  $\mu_{\sigma_{q,\psi}^2}$ :

$$\mu_{\sigma_{c_k|\varphi}^2} \le M \cdot \min\left(\frac{1}{4}, \frac{\sigma_n}{2\sqrt{\pi}}\right) \dots (28)$$

## 3.4. Variance of the conditional mean

The dependence of the term  $\sigma^2_{\mu_{q;\sigma}}$  on the number of samples and the transition voltage is less monotone then the term  $\mu_{\sigma^2_{q;\sigma}}$  (Figure 4).



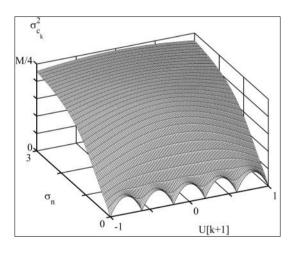
**Figure 4** Representation of the term  $\sigma^2_{\mu_{e_k|e_k}}$  as a function of the normalized transition voltage (*U*[*k*+1]) and the input equivalent noise ( $\sigma_n$ ).

For small values of the standard deviation of the noise the term  $\sigma^2_{\mu_{\alpha,\varphi}}$  depends strongly on the transition voltage. The number of arcs seen in Figure 4 is equal to the number of samples.

The maximum value of this term occurs in the absence of noise and it is equal to <sup>1</sup>/<sub>4</sub>.

## 3.5. Variance of the number of counts

The variance of the number of counts is determined by adding the terms  $\mu_{\sigma_{ck}^{0}}$  and  $\sigma_{\mu_{ck}}^{2}$  (Figure 5).



**Figure 5** Representation of the term  $\sigma_{c_k}^2$  as a function of the normalized transition voltage and of the standard deviation of the input-equivalent noise for the case of M = 5

Combining the conclusions from the two previous sections we reach the following expression for the upper limit of the variance of the number of counts.

### 3.6. Variance of the transition voltages

The determination of the variance of the estimated transition voltages is, using (1) [11]:

Inserting (29) leads to

#### 4. Variance of the Code Bin Widths

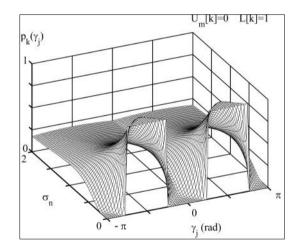
#### 4.1. Probability of a given sample belonging to a bin of the histogram

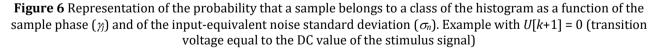
The code bin widths are computed from the histogram using (2). The analysis of the variance of the number of counts of the histogram is similar than the one presented for the cumulative histogram. The first difference is that the probability of a given sample belonging to a bin k of the histogram is given by

$$p_{k}(\gamma_{j}) = \int_{U[k]}^{U[k+1]} f_{u_{j}}(u \mid \gamma_{j}) \cdot du =$$

$$= \frac{1}{2} erf\left(\frac{U[k+1] + tri(\gamma_{j})}{\sqrt{2} \cdot \sigma_{n}}\right) - \frac{1}{2} erf\left(\frac{U[k] + tri(\gamma_{j})}{\sqrt{2} \cdot \sigma_{n}}\right) \qquad (32)$$

Instead of (10). This probability is depicted in Figure 6 as a function of sample phase,  $\gamma_j$  and input-equivalent noise standard deviation,  $\sigma_n$ .





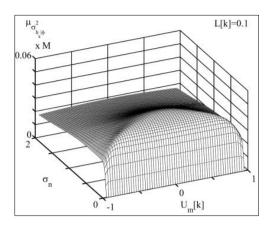
Note that instead of using U[k] and U[k+1] it is easier to use the mean value of the normalized transition voltages and the code bin width given, in normalized units, by  $U_m[k]$  and L[k]:

$$U_m[k] = \frac{U[k+1] + U[k]}{2} , \ L[k] = U[k+1] - U[k]$$
.....(33)

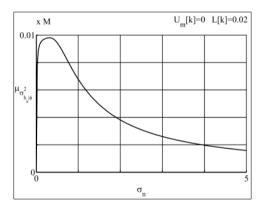
#### 4.2. Mean of the conditional variance

The mean of the conditional variance of the number of counts of the histogram is obtained using (18) and (32). The result, as a function of the input-equivalent noise standard deviation and mean normalized transition voltage is depicted in Figure 7.

In Figure 8 the mean of the conditional variance is depicted as a function of the input-equivalent noise for a null mean transition voltage and a normalized code bin width of 0.02 (2% of the stimulus signal amplitude).



**Figure 7** Representation of the term  $\mu_{\sigma_{h_k}}$  as a function of the standard deviation of the input-equivalent noise ( $\sigma_n$ ) and of the normalized transition voltage



**Figure 8** Representation of the term  $\mu_{\sigma_{c_k \varphi}^2}$  as a function of the standard deviation of the input-equivalent noise ( $\sigma_n$ ) Figure 9 depicts the same term for small values of noise standard deviation.

**Figure 9** Representation of the term  $\mu_{\sigma_{h_{v}}^2}$  as a function of the standard deviation of the input-equivalent noise ( $\sigma_n$ ). The dashed line represents the approximation for low values of noise standard deviation given by (34). The dotted line represents the maximum values attained by the mean of the conditional variance

When examining the impact of low noise standard deviation, it becomes evident that the mean behavior of the conditional variance closely approximates a straight line. To calculate the slope of this linear relationship, we can recall that the number of counts in bin k of the histogram is essentially the difference between the number of counts in bin k+1 and k of the cumulative histogram, as indicated in equation (3).

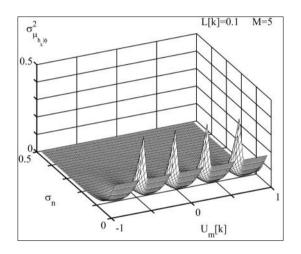
For situations with low noise standard deviation, the transition voltages (and correspondingly, the number of counts in the cumulative histogram) tend to become uncorrelated. Under these circumstances, the variance of the number of counts in the histogram is simply twice the variance of the number of counts in the cumulative histogram, as defined by equation (27):

$$\mu_{\sigma_{h_k|\varphi}^2} = \frac{M}{\sqrt{\pi}} \cdot \sigma_n \qquad (34)$$

This equation represents the dashed line in Figure 9. In Figure 8 and Figure 9 we can see that the mean of the conditional variance has a maximum of L[k]/2.

## 4.3. Variance of the conditional mean

The variance of the conditional mean of the histogram (Figure 10) has a similar behavior than it had in the case of the cumulative histogram (Figure 4).



**Figure 10** Representation of the term  $\sigma^2_{\mu_{h_k|\sigma}}$  as a function of the normalized transition voltage (*U*[*k*+1]) and the input equivalent noise ( $\sigma_n$ )

In the case of the cumulative histogram, for small values of noise standard deviation, the variance of the conditional mean approached  $\frac{1}{4}$ . In this situation the number of counts in the cumulative histogram are uncorrelated and the variance of the number of counts of the histogram will be twice that of the cumulative histogram, that is,  $\frac{1}{2}$ .

### 4.4. Variance of the number of counts

Using the conclusion that where reached in 4.2 and 4.3 we suggest the following expression for the upper limit of the number of counts of the histogram:

$$\sigma_{h_k}^2 \le \max\left(\frac{1}{2}, \min\left(\frac{L[k]}{2}, \frac{\sigma_n}{\sqrt{\pi}}M\right)\right) \tag{35}$$

## 4.5. Variance of the code bin widths

The values of the code bin widths can be obtained directly from the number of counts of the histogram.

The variance of the code bin widths, considering (2), can thus be calculated by [25]

Substituting (35) in (36) leads to

## 5. Conclusion

In this research paper, we have successfully derived mathematical expressions that facilitate the precise determination of transition voltages and code bin widths for analog to digital converters (ADCs) tested with the Histogram Test Method using triangular stimulus signals. These expressions take into account the influence of input-equivalent random noise, both in the test setup and within the ADC itself. By incorporating these factors, we are able to compute an uncertainty interval for the estimates, adhering to modern measurement practices similar to those recommended in the Guide to the Expression of Uncertainty in Measurement (GUM). The intent of this uncertainty interval is to effectively express the quality and reliability of the measurements obtained during the ADC testing process.

As the Histogram Test Method involves a statistical approach where a considerable number of samples are acquired, it is crucial to optimize the test time by determining the minimum number of samples required. The mathematical expressions derived in this study play a pivotal role in calculating the necessary sample size, allowing us to minimize the testing duration without compromising the accuracy and validity of the results.

In summary, this paper not only provides valuable insight into accurately characterizing ADCs with the Histogram Test Method and triangular stimulus signals but also offers a means to evaluate and express the uncertainty in the obtained measurements. Additionally, the knowledge of the minimum required number of samples contributes to efficient testing practices, saving time and resources while maintaining the integrity of the testing process. These contributions collectively enhance the understanding and applicability of the Histogram Test Method for ADC characterization and promote the adoption of standardized and reliable testing methodologies in the field of electronics and signal processing.

## **Compliance with Ethical Standards**

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## Disclosure of conflicts of interest

There are no conflicts of interest to report.

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